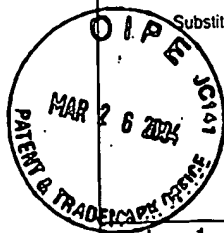


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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application Number	10/730,388
Filing Date	12/7/2003
First Named Inventor	Miguelanez
Art Unit	2857
Examiner Name	Not yet assigned
Attorney Docket Number	TAI.0800

Sheet 1 1 of 1 2

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No.¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code² (if known)			
CH		US-2003/0014205 A1	01/2003	Tabor	
CH		US-2003/0140287 A1	7/2003	WU et al.	
CH		US-6,338,148 B1	1/2002	Gillenwater et al.	
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		US-			
		US-			
		US-			

FOREIGN PATENT DOCUMENTS

Examiner Initials.	Cite No.¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T³
		Country Code² Number³ Kind Code⁴ (if known)				
CH		EP 1067392 A2	06/29/2000	Lejune		
Examiner Signature		<i>[Signature]</i>			Date Considered	5/10/05

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		<i>Complete if Known</i>	
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		Examiner Name	Not yet assigned
Sheet 2 of 2	Attorney Docket Number	TAI.0800	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		"Automatically Analyzing Effects of Process Equipment on Yield", http://www.semiconductor.net/semiconductor/issues/issues/1999/aug99/docs/yield.asp .	
		"Capability Ratios Vary", http://www.qualitydigest.com/june97/html/capability.html .	
		"Using SPC in Batch Processes" http://www.qualitydigest.com/mar98/html/spcbatch.html .	
		"Splitting Wafer Lots in Semiconductor Manufacturing, Part II: Comparing Two Processes - A Better Approach," http://www.semicondutoronline.com/content/news/article.asp?DocID=(66EE75EA-372B-11D4-8C3D-009027DE0829)&Bucket=Features&V	
		"Technique Closes the Loop on Test System Uncertainty," http://www.tnmworld.com/articles/05_1999_Technique_Loop.htm .	
		"How Much Measurement Error is Too Much? Part I: Modelling Measurement Data," http://www.testandmeasurement.com/content/news/article.asp?DocID=(592878CF-FC4E-11D3-8C2C-009027DE0829)&Bucket=HomeFeaturedArticles .	
		"Data Conductor, Solution for Semiconductor Yield Enhancement"	
		"Predictive Smoothing Algorithm Theory of Operation"	

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